

# The Results of Software Complex OPTAN Use for Modeling and Optimization of Standard Engineering Processes of Printed Circuit Boards Manufacturing

Olexander Bazylyk, Pavlo Taradaha, Oleg Nadobko, Lyubomyr Chyrun, Tetyana Shestakevych

**Abstract** – The results of software OPTAN testing on the examples of modeling and optimization of technological processes of printed circuit boards manufacturing using various techniques are presented. The software efficiency is confirmed, and ways of the technological processes research improvement are suggested.

**Keywords** – software complex OPTAN, technological processes modeling, modeling and optimization of technological processes of printed circuit boards manufacturing.

## INTRODUCTION

At the Lviv Polytechnic National University under the scientific guidance of professor Nedostup L. A. a group of authors developed scientific basis and methodology of modeling and optimizing of radio-electronic equipment (REE) at the stage of its manufacturing [1-4]. Nowadays, a universal software complex OPTAN was developed on this basis. The complex allows to solve following practical tasks:

- to build a structural, formal, and mathematical models of technological processes (TP) of REE manufacturing, which implement the proposed methodology of modeling, and are suitable for the study of a wide class of real TP;
- to maintain basic (standard) TP options research to assess their effectiveness;
- to optimize the TP with criteria of the total minimum manufacturing and operating costs, which generally shows the possibilities of their improvement using providing optimal (rational) options of process control organizing.

## TESTING SOFTWARE COMPLEX

The software testing was conducted on examples of printed circuit boards manufacturing using various technologies, including combined positive method of circuit boards manufacturing with metallized holes at foiled material (subtractive method), the boards manufacturing at non-foiled materials with adhesive layer (semi-additive method), and multilayer circuit board manufacturing with through-holes metallization method. The choice of these TP research was made due to the following considerations. First, mentioned processes are a group of standard manufacturing processes, which are widely used. A sufficient amount of relevant information concerning defects at various stages of TP, the cost of defects removal, the impact on service and maintenance, etc. was accumulated. Secondly, the study

indicated processes were already partially carried out using different software that allows to compare results and draw conclusions about the effectiveness of the developed software in general.

For the above TP structural, formal, and mathematical models were built, information database were generated, basic variants of TP were studied, and the results of optimization of control depth with the minimum total manufacturing costs criteria were obtained.

The example at Figure 1 shows the input window for information about the TP of printed boards manufacturing with combined positive method and its technical and economic characteristics (model name, the processing steps list, the probability parameters matrix, cost parameters, etc.).

Fig. 1. An input information window

The example of information in the matrix form input is shown at Fig. 2.

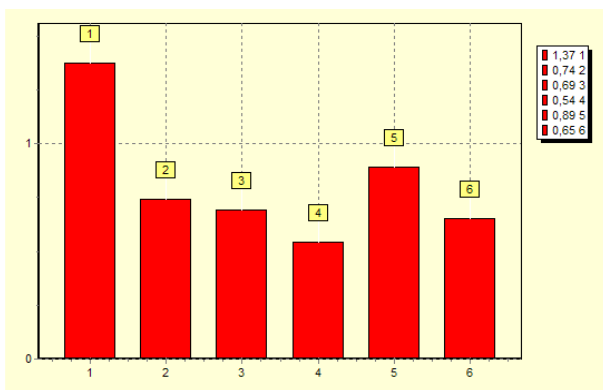
	1	2	3	4	5	6
1	0,95021	0,17995	0,09502	0,00000	0,00000	0,00000
2	0,00000	0,89974	0,09502	0,17009	0,00000	0,00000
3	0,00000	0,00000	0,95021	0,17009	0,17995	0,00000
4	0,00000	0,00000	0,00000	0,85043	0,44987	0,00000
5	0,00000	0,00000	0,00000	0,00000	0,89974	0,47511
6	0,00000	0,00000	0,00000	0,00000	0,00000	0,95021

Olexandr Bazylyk - Lviv Polytechnic National University, S. Bandery Str., 12, Lviv, 79013, UKRAINE, E-mail: namepodlec@yandex.ru

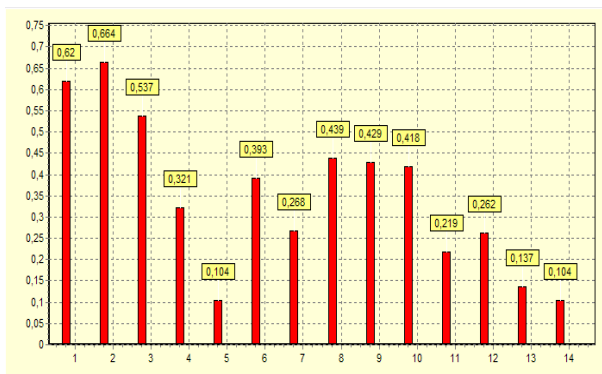
Fig. 2. The way of correct controls probability matrix input

	1	2	3	4	5	6
1	1-1	2-1	3-1	4-1	5-1	6-1
2		2-2	3-2	4-2	5-2	6-2
3			3-3	4-3	5-3	6-3
4				4-4	5-4	6-4
5					5-5	6-5
6						6-6

Fig. 3. The information output about standard (basic) version of TP



a



b

Fig. 4. The results of depth control optimization: a - for TP of printed boards with combined positive method manufacturing and b - for TP of boards manufacturing with subtractive method

The results of a standard (basic) TP version are formed as given at Fig. 3. It shows the 6-step model of studied TP of printed circuit boards manufacturing. Additionally it is

possible to obtain necessary information for each technological operation (given at the window bottom).

At Fig. 4 the results of optimization of depth control for two standard TP - technological processes of printed circuit boards manufacturing with the combined positive method, and boards with metallized holes on foiled material manufacturing are shown. As seen, the first one is presented in the form of 6-steps process, and the second – in the form of 14-steps process. As one of the possible optimization methods the stepwise optimization method, which provides alternate search of optimal values of the depth control for each step of TP, is used.

## CONCLUSIONS

1. Comparative analysis of the obtained results confirmed the efficiency of the developed software, and also showed possible improvement options of analyzed TP using optimizing control procedures.

2. Due to results shown at fig. 4a the possibility of some control requirements redistribution is fixated - more stringent requirements should be transferred to the initial steps of TP. For TP at Fig.4.b optimal values of depth control less then standard are set. This may indicate the possibility of control operations requirements relaxation, for example, the transition to the sampling methods, some control operations association, and so on.

## LITERATURE

- [1]Забезпечення якості та надійності радіоелектронних пристроїв шляхом комплексної оптимізації процесів виробництва / Недоступ Л.А., Бобало Ю.Я., Кіселичник М.Д., Лазько О.В. // Вісник Нац. ун-ту „Львів. політехніка”. - 2005. - № 534 : Радіоелектроніка та телекомунікації. - Львів: Вид-во Нац. ун-ту „Львів. політехніка”, 2005. – С. 45-51..
- [2]Імовірнісна формалізація процесів утворення, виявлення і пропуску дефектів на стадіях життєвого циклу РЕА / Бобало Ю.Я., Недоступ Л.А., Кіселичник М.Д., Лазько О.В. // Вісник Нац. ун-ту “Львів. політехніка”. - 2007. - № 595 : Радіоелектроніка та телекомунікації. - С.57-61.
- [3] Бобало Ю.Я. Керування процесами формування та контролю заданих властивостей у виробництві електронних пристроїв / Бобало Ю.Я., Недоступ Л.А., Кіселичник М.Д. // Вісник Нац. ун-ту “Львів. політехніка”.-2009. - № 637 : Електроенергетичні та електромеханічні системи. - С. 7-11.
- [4]Бобало Ю.Я. Моделювання та керування процесами формування та контролю якості радіоелектронної апаратури // Восточно-європейський журнал передових технологій. – 2009. – № 2/3 (38)